

Electronic Acknowledgement Receipt

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Application Number:	10572730
International Application Number:	
Confirmation Number:	6903
Title of Invention:	Semiconductor Device Having High-K Gate Dielectric Layer and Method For Manufacturing the Same
First Named Inventor/Applicant Name:	Hiroshi Oji
Customer Number:	54068
Filer:	Joseph Richard Keating/Rebecca Cordeau
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Application Type:	U.S. National Stage under 35 USC 371

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed	40404-30-PTO-20080723-ID S_Form_SB_08a.pdf	732947 4a3241c02043d5558bad642e4b2dd81f71f99efd	no	4

Warnings:

Information:

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.